PCIe UltraScale+ VU13P FPGA Board
Power Edition for Cryptocurrency Mining

BittWare’s CVP-13 is an UltraScale+ VU13P FPGA-based PCIe card designed for ultra high power applications. The Xilinx UltraScale+ VU13P FPGA gives designers incredible performance potential, with 3.8M logic elements —yet with a power density that makes power and thermal management difficult. The CVP-13 meets this challenge with BittWare’s Viper platform, supporting monster FPGA loads, up to 256 GBytes DDR4 or 1152 Mbits of QDR-II+, and up to 800Gbps board-to-board bandwidth.

The CVP-13 supports liquid cooling and a 300A FPGA core power supply. While the board is available in an air-cooled version, the liquid cooling option unleashes the full potential of the CVP-13. Two external 8-pin power connectors provide all necessary power.

The CVP-13’s Board Management Controller (BMC) provides full system monitoring and control, including core voltage adjustment, automatic shut-down to avoid board damage, and recovery from shut-down.

**key features**

- **300A FPGA core power supply**
- **Liquid Cooling option for extreme FPGA loads**
- **Viper platform for enhanced cooling performance**
- **Xilinx VU13P FPGA**
  - 3.8 million LCs
  - 360Mb UltraRAM
- **Gen3 x16 PCIe**
- **USB for BMC, FPGA JTAG, and FPGA UART**
- **4x QSFP28s for 400Gbps board-to-board bandwidth**
- **2x UltraPort SlimSAS for additional 400Gbps board-to-board bandwidth**
- **16nm FPGA with up to 3.8 million LEs**
- **2x DIMMs for up to 256 GBytes DDR4 or 1152 Mbits QDR-II+**
- **Board Management Controller for Intelligent Platform Management**
High-Speed Connectivity
The CVP-13 is enabled for high-speed connectivity with four front panel QSFP28 cages, supporting 400Gbps. Two UltraPort SlimSAS connectors provide another 400Gbps.

USB Interface
A USB interface is provided for debug and programming support. It allows access to the BMC for system monitoring and control, FPGA UART for bitstream connectivity, and FPGA JTAG for Vivado programming and debug.

System Management
For system management, the CVP-13 is equipped with a Board Management Controller (BMC), which accepts IPMI 2.0 commands. Use it along with BittWare’s BittWorks II Toolkit Lite to program the FPGA Flash over USB, monitor board power and temperature, re-program the onboard clocks, and adjust FPGA core voltage. The BMC monitors critical temperatures, voltages, and current and will shut the board down to prevent damage. Recovery from shutdown is also supported, without the need to cycle system power.

Bitstreams
The CVP-13 is supported by popular bitstream developers. It can be ordered with encryption fuses burned to allow protected bitstreams to be used.

Board Specifications

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
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<tbody>
<tr>
<td><strong>FPGA</strong></td>
<td>• Virtex UltraScale+ VU13P</td>
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<tr>
<td></td>
<td>• 48x GTY transceivers at 32.75 Gbps</td>
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<tr>
<td></td>
<td>• 3.8 million logic elements</td>
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<td></td>
<td>• Over 400 Mb of embedded memory</td>
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<td>• Integrated PCIe core</td>
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<td>• 11,904 DSP slices with 27x18 multipliers</td>
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<tr>
<td><strong>On-board Flash</strong></td>
<td>2Gbit Flash memory for storing FPGA bitstreams</td>
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<td><strong>Optional DIMM memory</strong></td>
<td>2 DIMM sites, each supporting:</td>
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<tr>
<td></td>
<td>• Up to 128 GBytes DDR4 x72 with ECC</td>
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<tr>
<td></td>
<td>• Up to 576 Mbits dual QDR-II+ x18 (2 independent 288 Mbit banks)</td>
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<td><strong>PCIe interface</strong></td>
<td>x16 Gen1, Gen2, Gen3 interface direct to FPGA (optional; no power used from PCIe connector)</td>
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<tr>
<td><strong>USB interface</strong></td>
<td>Provides access to</td>
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<tr>
<td></td>
<td>• BMC</td>
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<td></td>
<td>• FPGA UART</td>
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<td>• FPGA JTAG (for Vivado)</td>
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<td><strong>QSFP cages</strong></td>
<td>• 4 QSFP28 cages on front panel connected directly to FPGA via 16 GTY transceivers</td>
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<td>• Provides 400 Gbps board-to-board bandwidth</td>
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<tr>
<td><strong>UltraPort SlimSAS</strong></td>
<td>• 2 UltraPort SlimSAS on rear edge connected to FPGA via 16x GTY transceivers</td>
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<td>• Provides additional 400Gbps board-to-board bandwidth</td>
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Optional Development Tools (for Bitstream Developers)

- System development
  - BittWorks II Toolkit - host, command, and debug tools for BittWare hardware

- FPGA development
  - FPGA Examples - example Vivado projects
  - Xilinx Tools - Vivado® Design Suite

BWMonitor

- Live board power/temperature display is included as part of Toolkit Lite

Board Management Controller

- Voltage, current, temperature monitoring
- Power sequencing and reset
- Field upgrades
- FPGA configuration and control
- Clock configuration
- I/O bus access
- USB 2.0
- Voltage overrides

Power & cooling

- Liquid-cooled or air-cooled
- Dual external 8-pin power connectors (both must be connected)
- 300A FPGA core power supply

Size

- ¾-length, standard-height PCIe dual-slot card
- 254mm x 111.15mm
- Max. component height: 34.79mm dual slot