BittWare a **molex** company

IA-840f
PCIe FPGA Card

Agilex™ FPGA card with 3x QSFP-DDs
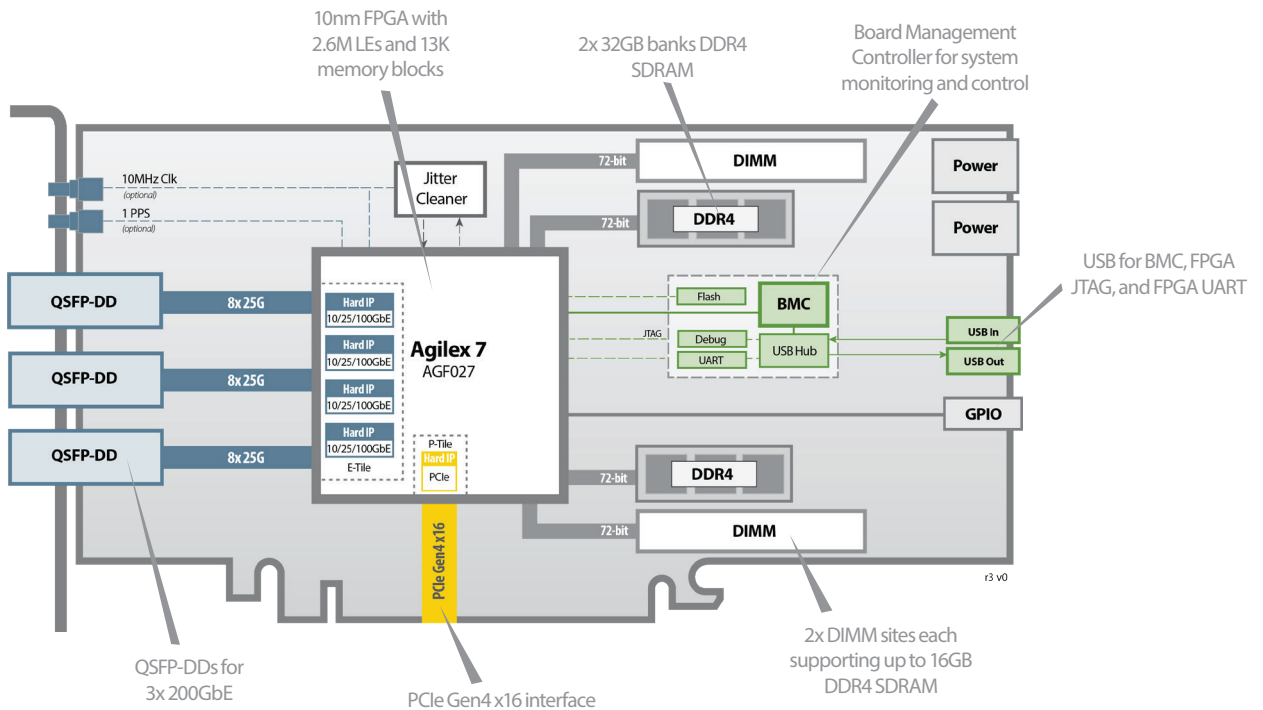## 3x 200GbE with up to 128GBytes DDR4 SDRAM

BittWare's IA-840f is an Altera Agilex™-based FPGA card designed to deliver up to 40% higher performance for data center, networking and edge compute workloads. BittWare maximized I/O features on the card using the Agilex chip's unique tiling architecture with three QSFP-DDs (3× 200G) and PCIe Gen4 x16. The card also supports Intel oneAPI™, which enables an abstracted development flow for dramatically simplified code re-use across multiple architectures.

1 oneAPI

intel. AGILEX™ 7

The IA-840f supports Intel's OneAPI open standards-based unified programming model

key features

| QSFP-DDs for 3x 200G | Intel OneAPI support | Altera Agilex™ FPGA with up to **2.6M Logic Elements** |

10nm FPGA with 2.6M LEs and 13K memory blocks

2x 32GB banks DDR4 SDRAM

Board Management Controller for system monitoring and control

USB for BMC, FPGA JTAG, and FPGA UART

QSFP-DDs for 3x 200GbE

PCIe Gen4 x16 interface

2x DIMM sites each supporting up to 16GB DDR4 SDRAM

Additional Services

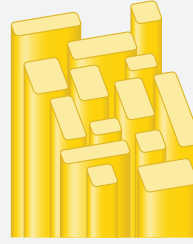Take advantage of BittWare's range of design, integration, and support options

Customization
[Additional specification options or accessory boards](#) to meet your exact needs.
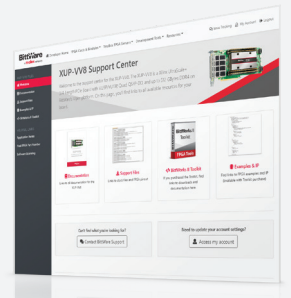
Server Integration
Available pre-integrated in our [TeraBox servers](#) in a range of configurations.

Application Optimization
Ask about our services to help you port, optimize, and benchmark your application.

Service and Support
BittWare Developer Site provides online documentation and issue tracking.

Board Specifications

FPGA	• Altera Agilex 7 F-Series
	  • AGF027 in an R2581A package
	  • Core speed grade -2: I/O speed grade -2
	• Contact BittWare for other Agilex FPGA options

On-board Flash	• 2Gbit Flash memory for booting FPGA
External memory	• 2x 288-pin DIMM slots, each supporting up to 16GB DDR4 SDRAM modules (up to 32GB total) • 2x banks on-board DDR4, up to 32GB each
Host interface	• x16 Gen4 interface direct to FPGA, connected to PCIe hard IP
QSFP-DD cages	• 3 QSFP-DD cages on front panel connected directly to FPGA via 24 transceivers • User programmable low jitter clocking supporting 10/25/40/100GbE • Each QSFP-DD can be independently clocked • Jitter cleaner for network recovered clocking • Multi-rate hard MAC+FEC for 10/25/100GbE (4x HardIP) • Fully backward compatible with QSFP28s
External clocking	• 1 PPS and 10MHz ref clk front panel inputs (optional)
USB	• USB access to BMC, USB-JTAG, USB-UART

Board Management Controller	• Voltage, current, temperature monitoring • Power sequencing and reset • Field upgrades • FPGA configuration and control • Clock configuration • Low bandwidth BMC-FPGA comms with SPI link • USB 2.0 • PLDM support • Voltage overrides
Cooling	• Standard: dual-slot passive heatsink • Optional: dual-slot liquid cooling
Electrical	• On-board power derived from PCIe slot 12V and two AUX connectors • Power dissipation is application dependent • Typical max power consumption 225W
Environmental	• Operating temperature: 5°C to 35°C
Quality	• Manufactured to IPC-A-610 Class 2 • RoHS compliant • CE, FCC, UKCA & ICES approvals
Form factor	• Standard-height, dual-slot PCIe card • 111.15mm x 266.70mm (4.376in x 10.500in)

Development Tools

System development	BittWare SDK including PCIe driver, libraries, and board monitoring utilities
Application development	Supported design flows - Intel FPGA oneAPI Base Toolkit, Intel High-Level Synthesis (C/C++) and Quartus Prime Pro (HDL, Verilog, VHDL, etc.)

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To learn more, visit **www.BittWare.com**

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